# UNITED STATES PATENT AND TRADEMARK OFFICE
## CERTIFICATE OF CORRECTION

PATENT NO.       : 6,944,694 B2
APPLICATION NO. : 09/903161
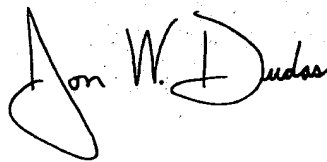DATED          : September 13, 2005
INVENTOR(S)     : Pax

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 1, line 9 "unil" should read --unit.--;
Col. 1, line 46 "constrcted" should read --constructed--;
Col. 3, line 28 "AJtematively," should read --Alternatively,--;
Col. 4, line 3 "1048" should read --104B--;
Col. 4, line 10 "10" should read --110--;
Col. 4, line 54 "infomation" should read --information--;
Col. 5, line 67 "206," should read --206.--;
Col. 7, line 66 "mnemory" should read --memory--;
Col. 13, line 16 "arrangements;" should read --arrangement;--;
Col. 13, line 21 "processors," should read --processor,--;
Col. 13, line 34 "and" should read --said--;
Col. 13, line 45 "a" should be deleted;
Col. 13, line 62 "arrangement;" should read --assignment;--;
Col. 14, line 30 "arranging" should read --rearranging--;
Col. 14, line 61 "position" should read --positions--;
Col. 17, line 45 "address" should read --address bus--;
Col. 19, line 52 "multiplexers" should read --multiplexer--;
Col. 20, line 38 "inputs," should read --input,--;
Col. 21, line 12 "bank;" should read --banks--;
Col. 21, line 59 "p1" should be deleted.

Signed and Sealed this

First Day of August, 2006

JON W. DUDAS
*Director of the United States Patent and Trademark Office*